

FORM PTO-1449 (Rev. 2-32)	U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No.	Serial No.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		01-1205-A	10/658,710
		Applicant: Elad Alon, et al.	
		Filing Date: 09/08/2003	Group:

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriat
Al	5,614,855	03-1997	Lee et al	327	158	8/21/95
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FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation
							Yes
							No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>HL</i>	<i>JL</i>	Jung-Bae Lee, et al., <i>Digitally-Controlled DLL, and I/O Circuit for 500Mb/s/pin x16 DDR SDRAM, ISSCC 2001, Session 4 High-Speed Digital Interfaces, 4.6, February 5, 2001, 2 pages</i>
EXAMINER	<i>LUL</i>	DATE CONSIDERED <i>3/31/04</i>

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.